

AMENDMENTS TO THE CLAIMS

The listing of claims will replace all prior versions, and listings, of claims in the application:

Listing of Claims:

1. (Currently Amended) A computer-implemented method for managing a plurality of processors as a virtual devices, said method comprising:

receiving a device request at a task queue manager running on a first processor in a computer system, wherein the computer system includes a plurality of heterogeneous processors that share a common memory and wherein the device request corresponds to a virtual device;

storing data corresponding to the request in the common memory;

identifying a second processor from the plurality of processors to handle the request, wherein the first processor and the second processor are dislike processors, wherein the first processor executes a first instruction set and wherein the second processor executes a second instruction set;

creating, by the first processor, a task block in the common memory, the task block including a software code identifier and an input buffer address;

signaling, from the first processor, the identified second processor, wherein the signaling includes writing the address of the task block to a mailbox corresponding to the second processor;

receiving the data corresponding to the request at the second processor; and

receiving, at the second processor, the address of the task block from the second processor's mailbox;

retrieving, at the second processor, the software code identifier from the task block;

reading data from an input buffer located in the common memory at a location corresponding to the input buffer address into the second processor's local memory, wherein the reading is performed using a direct memory access (DMA) operation;

determining whether the software code corresponding to the software code identifier is loaded in the second processor's local memory; and
in response to determining that the software code corresponding to the software code identifier is not loaded in the second processor's local memory;

reading the software code from the common memory into the second processor's local memory, wherein the reading is performed using a DMA operation; and

processing the data by the second processor using the software code stored in the second processor's local memory.

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